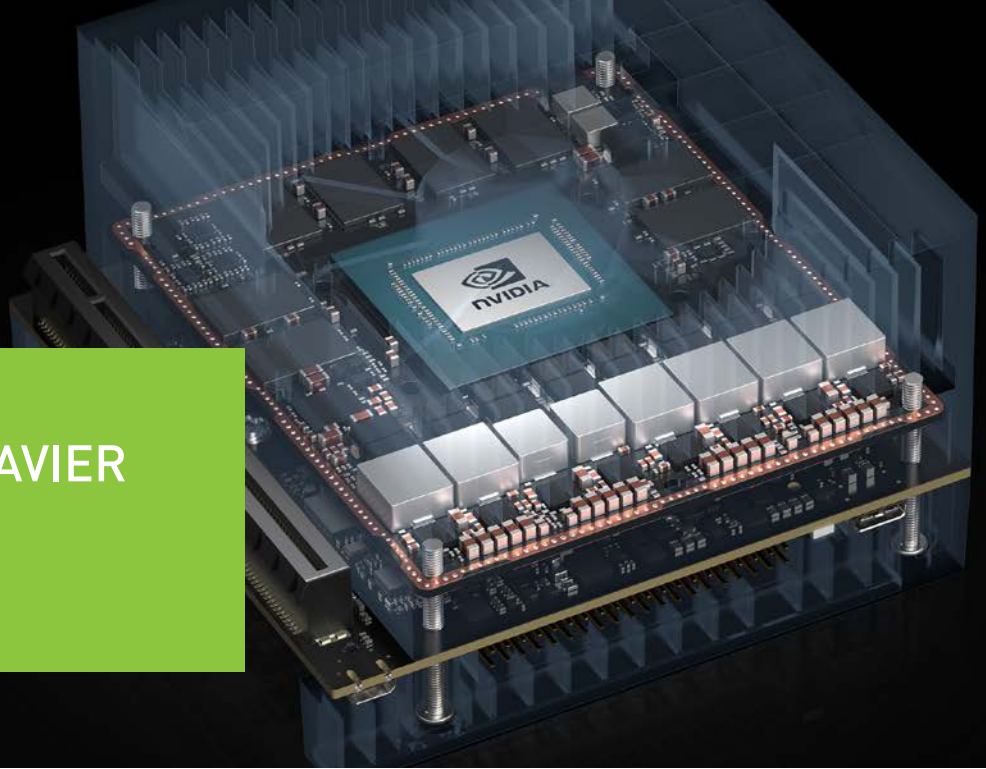




NVIDIA JETSON AGX XAVIER DEVELOPER KIT POWERING AI IN AUTONOMOUS MACHINES.



Usher in a new era of autonomous machines with the NVIDIA Jetson AGX Xavier Developer Kit.

Now, you can develop and deploy autonomous machines at scale with the powerful NVIDIA® Jetson AGX Xavier™ Developer Kit. It's capable of running modern, advanced neural networks and other AI workloads to solve problems in manufacturing, logistics, retail, service, agriculture, smart cities, and portable medical devices. Plus, it delivers up to 32 TOPS and can operate in as little as 10 W.

The Jetson AGX Xavier Developer Kit helps you speed innovation. As part of NVIDIA's world-leading AI platform, it's integrated with NVIDIA tools and workflows that let you train neural networks faster, which means more iterations and better accuracy.

It's also supported by NVIDIA CUDA®, cuDNN, and TensorRT™ software libraries, as well as our JetPack™ and DeepStream SDKs. This helps you boost performance while reducing development cost and effort.

Learn more about the Jetson AGX Xavier Developer Kit and get access to documentation, training, and tutorials at www.developer.nvidia.com/jetson-agx-xavier.



KEY FEATURES

Module

- > 512-Core NVIDIA Volta™ GPU with Tensor Cores
- > (2x) NVDLA Engines
- > 8-Core ARM® v8.2 64-Bit CPU
- > 32 GB 256-Bit LPDDR4x
- > 32 GB eMMC 5.1 Flash Storage
- > 7-Way VLIW Vision Accelerator Processor

Buttons

- > Power On/Off
- > Force Recovery
- > Reset

> Power Options

- > External 19 V AC Adapter

I/O

- > (2x) USB 3.1 Type C (10 GT/s)
- > PCIe x8/SLVS-EC x8
- > Gigabit Ethernet
- > (1x) Hybrid eSATA/USB 3.0 Type A
- > (3x) eDP/DP/HDMI at 4K @ 60 | HDMI 2.0, DP1.4
- > (16x) CSI-2 Lanes
- > M.2 Key E, M.2 Key M
- > Micro SD/UFS
- > UART, SPI, CAN, I²C, I²S, DMIC, GPIOs

NVIDIA JETSON AGX XAVIER

TECHNICAL SPECIFICATIONS

DEVELOPER KIT

GPU	512-Core Volta GPU with Tensor Cores
CPU	8-Core ARM v8.2 64-Bit CPU, 8 MB L2 + 4 MB L3
Memory	32 GB 256-Bit LPDDR4x 137 GB/s
Storage	32 GB eMMC 5.1
DL Accelerator	(2x) NVDLA Engines
Vision Accelerator	7-Way VLIW Vision Processor
Encoder/Decoder	(2x) 4K @ 60 12-Bit Support
Size	105mm x 105mm x 65mm
Deployment	Module (Jetson AGX Xavier)

DEVELOPER KIT I/Os JETSON AGX XAVIER MODULE INTERFACE

PCIe X16	x8 PCIe Gen4/x8 SLVS-EC
RJ45	Gigabit Ethernet
USB-C	2x USB 3.1, DP (Optional), PD (Optional) Close-System Debug and Flashing Support on 1 Port
Camera Connector	(16x) CSI-2 Lanes
M.2 Key M	NVMe
M.2 Key E	PCIe x1 + USB 2.0 + UART (for Wi-Fi/LTE) / I ² S/PCM
40-Pin Header	UART + SPI + CAN + I ² C + I ² S + DMIC + GPIOs
HD Audio Header	High-Definition Audio
eSATAp + USB3.0 Type A	SATA Through PCIe x1 Bridge + USB 3.0 (PD + Data for 2.5-inch SATA)
HDMI Type A/DP	HDMI 2.0, DP 1.4
uSD/UFS Card Socket	SD/UFS

Visit www.developer.nvidia.com/jetson-agx-xavier to learn more.

©2020 NVIDIA Corporation. All rights reserved. NVIDIA, the NVIDIA logo, CUDA, Jetson, Jetson AGX Xavier, NVIDIA JetPack, NVIDIA Volta, and TensorRT are trademarks and/or registered trademarks of NVIDIA Corporation in the U.S. and other countries. Other company and product names may be trademarks of the respective companies with which they are associated. ARM, AMBA and ARM Powered are registered trademarks of ARM Limited. Cortex, MPCore and Mali are trademarks of ARM Limited. All other brands or product names are the property of their respective holders. "ARM" is used to represent ARM Holdings plc; its operating company ARM Limited; and the regional subsidiaries ARM Inc.; ARM KK; ARM Korea Limited.; ARM Taiwan Limited; ARM France SAS; ARM Consulting (Shanghai) Co. Ltd.; ARM Germany GmbH; ARM Embedded Technologies Pvt. Ltd.; ARM Norway, AS and ARM Sweden AB. JAN20

