

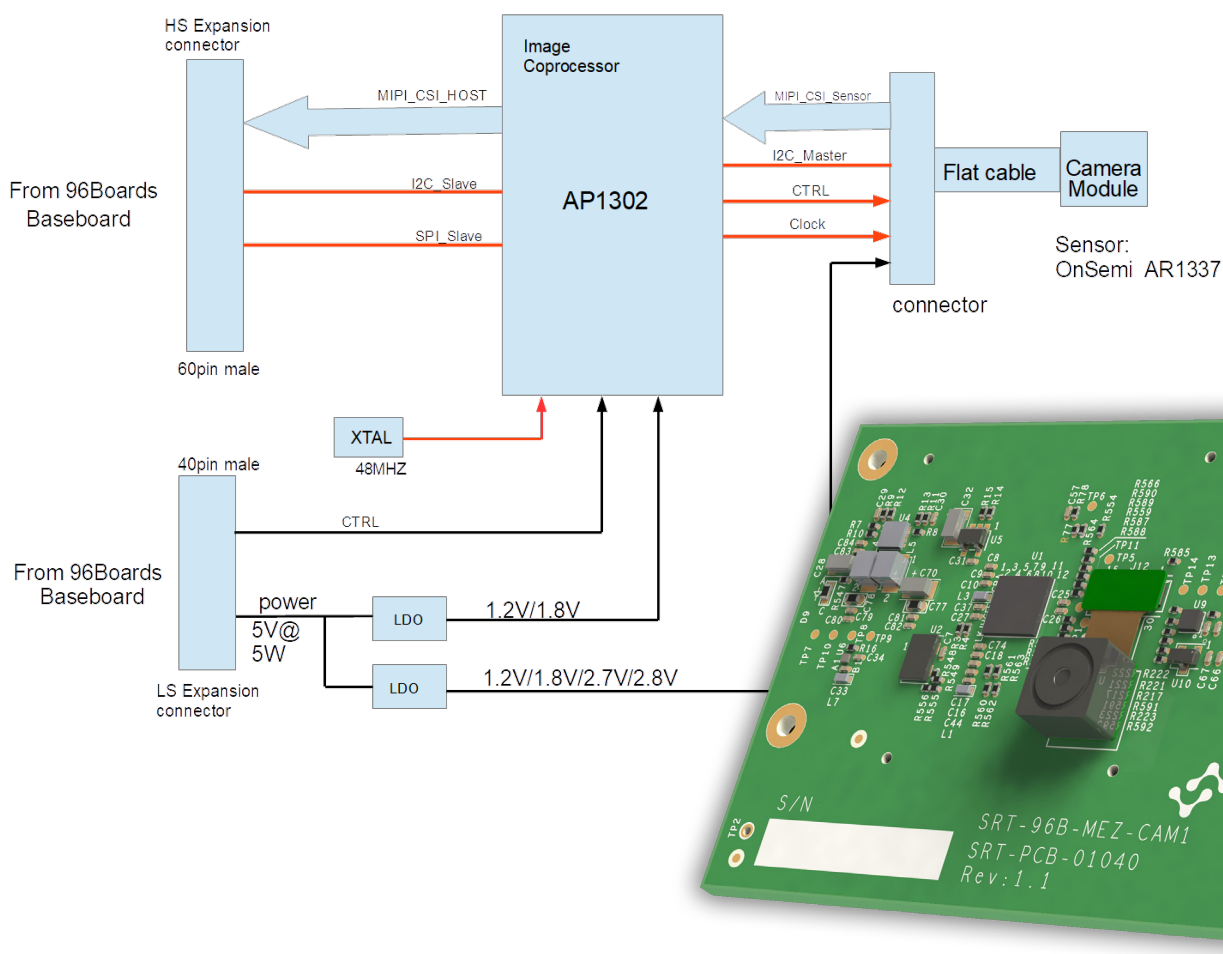
96Boards Camera1 Mezzanine

Featuring OnSemi AR1337 Imaging Sensor and AP1302 ISP

→ GET YOUR PRODUCT MOVING!

Camera 1 mezzanine is a 96Boards compatible add on board with OnSemi 13Mp imaging sensor and AP1302 ISP for evaluation and prototyping.

SRT-96B-MEZ-CAM1



96Boards Camera 1 Mezzanine

→ TECHNICAL SPECIFICATIONS:

MIPI Camera Module

AR1337 CMOS Imaging Sensor

Features:

- 13 MP CMOS imaging sensor
- SuperPD™ PDAF technology
- Unique PDAF pattern and micro-lens technologies
- On-chip bad pixel correction and AF calculations
- 1.1µm pixels providing an industry standard 1/3.2" optical format
- High Quantum Efficiency and Sensitivity with Low read noise
- Supports 13 MP at 30 frames per second
- Supports 4k2k video at 30 frames per second and Full HD 1080P video up to 60 frames per second

AP1302 Image Coprocessor

Advanced Image Coprocessor for ON Semiconductor's Sensors

Features:

- Optimized for operation with ON Semiconductor's Clarity+™ Pixel technology sensors as well as Bayer pattern CMOS Image Sensors (CIS)
- Up to 13 Mp (4224x3156) sensor support
- Designed to support ON Semiconductor sensors that provide video interlaced HDR (iHDR)
- 450 Mp/second processing (13Mp at 30/1080p at 120/720p at 240 – subject to sensor limitations)
- Color gaining and gamma correction
- Frame rate reduction, image resizing and clipping
- Auto exposure, auto white balance, auto focus, auto flicker detection and mitigation

- Adaptive Tone Mapping, Local Tone Mapping
- Face detection
- Smooth digital zoom and panning
- OSD, special effects
- Test pattern generator
- Programmable JPEG encoder with EXIF header support
- Scalado SpeedTags™ Encode support
- Two-wire serial interface (I2C) for sensor and peripheral control and register access supporting Standard (100kbps), Fast mode (400kbps), FM+ (1Mbps) and HS (3.4 Mbps)
- Dual on-chip 32-bit RISC processor cores
- Dual sensor support (second camera or 3D bridge application)
- 6 MIPI data lanes shared between two sensor interfaces (4+2/ 3+3)
- Four-wire serial interface (SPI slave) for register access supporting up to 25 Mbps
- 12 GPIOs (shared functionality with SPI master and slave, second I2C master)
- Fail-safe IO, programmable slew-rate control

ORDERING OPTIONS

96Boards Camera 1 Mezzanine P/N: SRT-96B-MEZ-CAM1

ON Semiconductor®



ANALOG
Five Years Out

96Boards



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